

AMENDMENTS TO THE SPECIFICATION

**Please amend paragraph [0059] as follows:**

**[0059]** Fig. 12 is a schematic diagram for one embodiment of shift enable circuits [[1102]] 1002 and 1003 of Fig. 10. The pictured embodiment includes reset flip-flops 1201-1205, inverters 1206-1210, NOR gates 1211-1215, and NAND gates 1216-1219. Each shift enable circuit provides four shift enable signals SA-SD, which in circuit 1002 provide shift left enable signals SLA-SLD, and in circuit 1003 provide shift right enable signals SRA-SRD.